

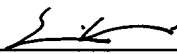
REMARKS

Further to the Amendment filed June 30, 2003, pages 6 and 7 of the specification have been amended to clarify the "third aspect" of the present invention disclosed in the *Summary of the Invention*. Specifically, due to an inadvertent translation error, the "second aspect" disclosed on page 6, lines 4-17 was the same as the "third aspect" disclosed beginning on page 6, line 18. Claim 3 has also been amended herewith to change "fixed clock signal" to --reference clock signal--. No new matter is added.

These changes are supported by embodiment 5 of the specification disclosed in the *Description of the Preferred Embodiments* and specifically the paragraph bridging pages 44-45 thereof, which teaches that an analog image signal is converted into a digital signal by A/D conversion at a sampling timing due to a fixed clock signal. Page 21, lines 2-4 of the specification further discloses that a "fixed clock" signal operates at a constant frequency on the basis of a reference clock signal. Thus, taken together, the specification supports the step of performing sampling and A/D conversion on an analog image signal on the basis of a reference clock signal.

Should the Examiner believe that anything further would be desirable to place this application in better condition for allowance, the Examiner is invited to contact the Applicant's undersigned attorney at the telephone number listed below.

Respectfully submitted,



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